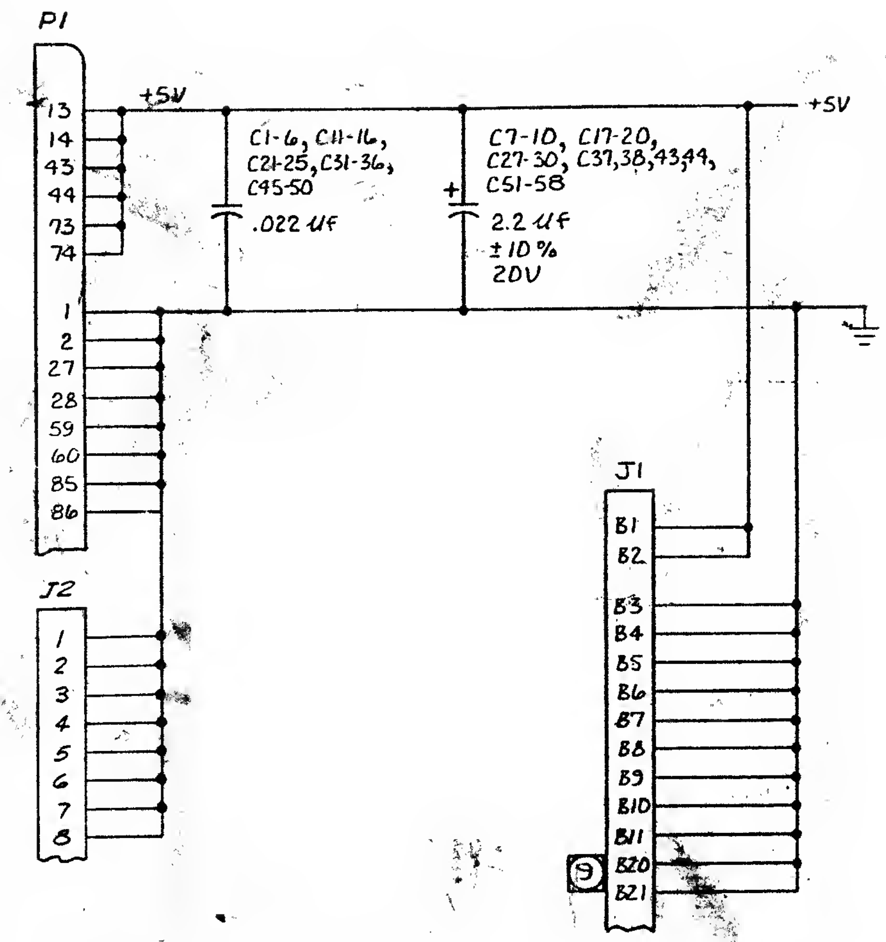
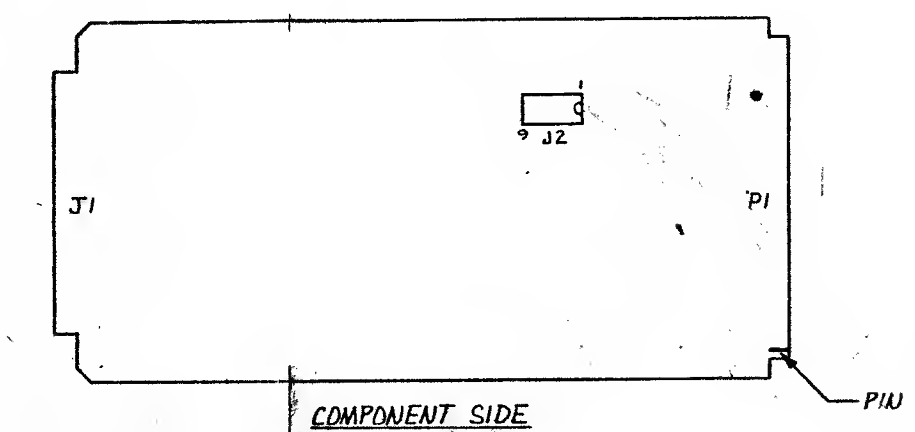
REV	DESCRIPTION	DR	CH	DATE	APPROVAL
GO	PROD REL PER 4625				





COMPONENT SIDE

UNUSED GATES/IC			
IC DESIGN.	IC TYPE	UNUSED PINS -IN	OUT
1	7438	1,2 9,10 12,13	3 8 11
15	7408	4,5	6
25	SPARE	ALL	ALL
11	8097	15,14 12	13 11
19	7404	9,11,13	8,10,12
29	7486	4,5 9,10 12,13	6 8 11
34	7402	5,6 11,12	4 13
41	7432	12,13	11
43	7408	1,2 4,5 12,13	3 6 11
52	7432	9,10	8
40	7438	12,13	11

TABULATION BLOCK		
DASH NO.	DESCRIPTION	JUMPER
-00	BASIC	W1
-01	OBRS ONE SHOT ADDED	W2

(10) FOR -00 JUMPER W1 IS INSTALLED
FOR -01 JUMPER W2 IS INSTALLED

(9) PINS B20 AND B21 ON J1 ARE RESERVED FOR MODULE CONTROL SIGNAL GROUND RETURNS ONLY.

8. CAPACITOR C26 IS OMITTED

7. ◯ DENOTES MULTI-ELEMENT DEVICES WITH COMMON FUNCTIONS AND COMMON PIN CONNECTIONS

6. —◁— DENOTES OUTPUT SIGNALS
—▷— DENOTES INPUT SIGNALS
—▷◁— DENOTES BI-DIRECTIONAL SIGNALS

5. ALL RESISTOR PACKS ARE 1K ±5%, 1/8 W.

4. ALL RESISTOR VALUES ARE IN OHMS, ±5%, 1/4W.

3. ALL CAPACITORS IN UF ALL .022 UF CAPACITORS ARE [illegible], -20%, 25V.

2. LAST REFERENCE DESIGNATORS USED: IC70, R5, C58, R27.

1. FOR LOGIC DASH NO. SEE TABULATION BLOCK

NOTES: UNLESS OTHERWISE SPECIFIED

FEB 04 1977

NOTES UNLESS SPECIFIED
1. TOLERANCES .XX ±.03 .XXX ±.010 ANGULAR ±½°
2. BREAK ALL SHARP EDGES .010 APPROX.
3. ALL DIM. IN INCHES

NEXT ASSEMBLYS

DR. | CHK. | DSG. | ENGR.

ComputerAutomation
18651 Von Karman, Irvine Calif 92664

TITLE: LOGIC DIAGKAM - 16 BIT I/O

SIZE: D | DWG. NO.: 00-52213-XX | REV: GO

DO NOT SCALE DRAWING | SCALE: NONE | SHT. 1 OF 6

REVISIONS					
REV	DESCRIPTION	DR	CH	DATE	APPROVAL

P1	SIGNAL	DIRECTION	LOCATION
1	GND	→	C8
2	GND	→	C8
3		N/U	
4			
5			
6			
7			
8			
9			
10			
11			
12		N/U	
13	+5V	→	C8
14	+5V	→	C8
15		N/U	
16			
17			
18			
19			
20			
21			
22			
23			
24			
25			
26		N/U	
27	GND	→	C8
28	GND	→	C8
29		N/U	
30			
31			
32			
33			
34			
35			
36			
37			
38		N/U	
39	$\overline{DBD0}$	↔	D32
40	$\overline{DBD1}$	↔	D32
41	$\overline{DBD2}$	↔	D32
42	$\overline{DBD3}$	↔	D32
43	+5V	→	C8

P1	SIGNAL	DIRECTION	LOCATION
44	+5V	→	C8
45	$\overline{DBD4}$	↔	C32
46	$\overline{DBD5}$	↔	C32
47	$\overline{DBD6}$	↔	C32
48	$\overline{DBD7}$	↔	C32
49	$\overline{DBD8}$	↔	C32
50	$\overline{DBD9}$	↔	C32
51	$\overline{DBI0}$	↔	B32
52	$\overline{DBI1}$	↔	B32
53	$\overline{DBI2}$	↔	B32
54	$\overline{DBI3}$	↔	B32
55	$\overline{DBI4}$	↔	B32
56	$\overline{DBI5}$	↔	B32
57	$\overline{EXEC}$	→	324
58	$\overline{IN}$	→	A24
59	GND	→	C8
60	GND	→	C8
61	$\overline{IØCL}$	→	A24
62	$\overline{OUT}$	→	A24
63			
64	$\overline{SER}$	←	D17
65	$\overline{IUR}$	←	B43
66			
67	$\overline{IAR}$	→	B24
68			
69	$\overline{RST}$	→	B24
70			
71	$\overline{PLSE}$	→	B24
72	$\overline{ECHØ}$	→	B24
73	+5V	→	C8
74	+5V	→	C8
75	$\overline{ABD03}$	→	D24
76	$\overline{ABD04}$	→	C24
77	$\overline{ABD05}$	→	C24
78	$\overline{ABD06}$	→	C24
79	$\overline{ABD07}$	→	C24
80	$\overline{ABD0D}$	→	C24
81	$\overline{ABD1}$	→	B24
82	$\overline{ABD2}$	→	B24
83	$\overline{PRIN}$	→	A24
84	$\overline{PRØT}$	←	B43
85	GND	→	C8
86	GND	→	C8

J1	SIGNAL	DIRECTION	LOCATION
A01	$\overline{PS4}$	→	C24
02	$\overline{PS3}$	→	C24
03	$\overline{PS2}$	→	C24
04	$\overline{PS1}$	→	D24
05	$\overline{PS0}$	→	D24
06	$\overline{E8}$	→	C44
07	$\overline{E16}$	→	C44
08	$\overline{E32}$	→	D44
09	$\overline{E64}$	→	D44
10	$\overline{E128}$	→	D44
11	$\overline{E256}$	→	D44
12	$\overline{ES7}$	→	C20
13	$\overline{ES6}$	→	C20
14	$\overline{ES5}$	→	C20
15	$\overline{ES4}$	→	C20
16	$\overline{ES3}$	→	C20
17	$\overline{ES2}$	→	C20
18	$\overline{ES1}$	→	D20
19	$\overline{ES0}$	→	D20
20	SPØL	→	A32
21	RPØL	→	C48
22	ØPØL	→	D30
23	IPØL	→	C37
24	PLSE	←	B22
25	$\overline{EX7}$	←	A19
26	$\overline{EX6}$	←	A19
27	$\overline{EX5}$	←	B19
28	$\overline{EX4}$	←	B19
29	$\overline{EX3}$	←	B19
30	$\overline{EX2}$	←	B19
31	$\overline{EX1}$	←	B19
32	$\overline{EX0}$	←	B19
33	ID15	→	A37
34	ID14	→	A37
35	ID13	→	A37
36	ID12	→	A37
37	ID11	→	B37
38	ID10	→	B37
39	ID09	→	B37
40	ID08	→	B37
41	ID07	→	C37
42	ID06	→	C37
43	ID05	→	C37
44	ID04	→	C37
45	ID03	→	D37
46	ID02	→	D37
47	ID01	→	D37
48	ID00	→	D37
49	C4	→	C26
A50	C2	→	B26

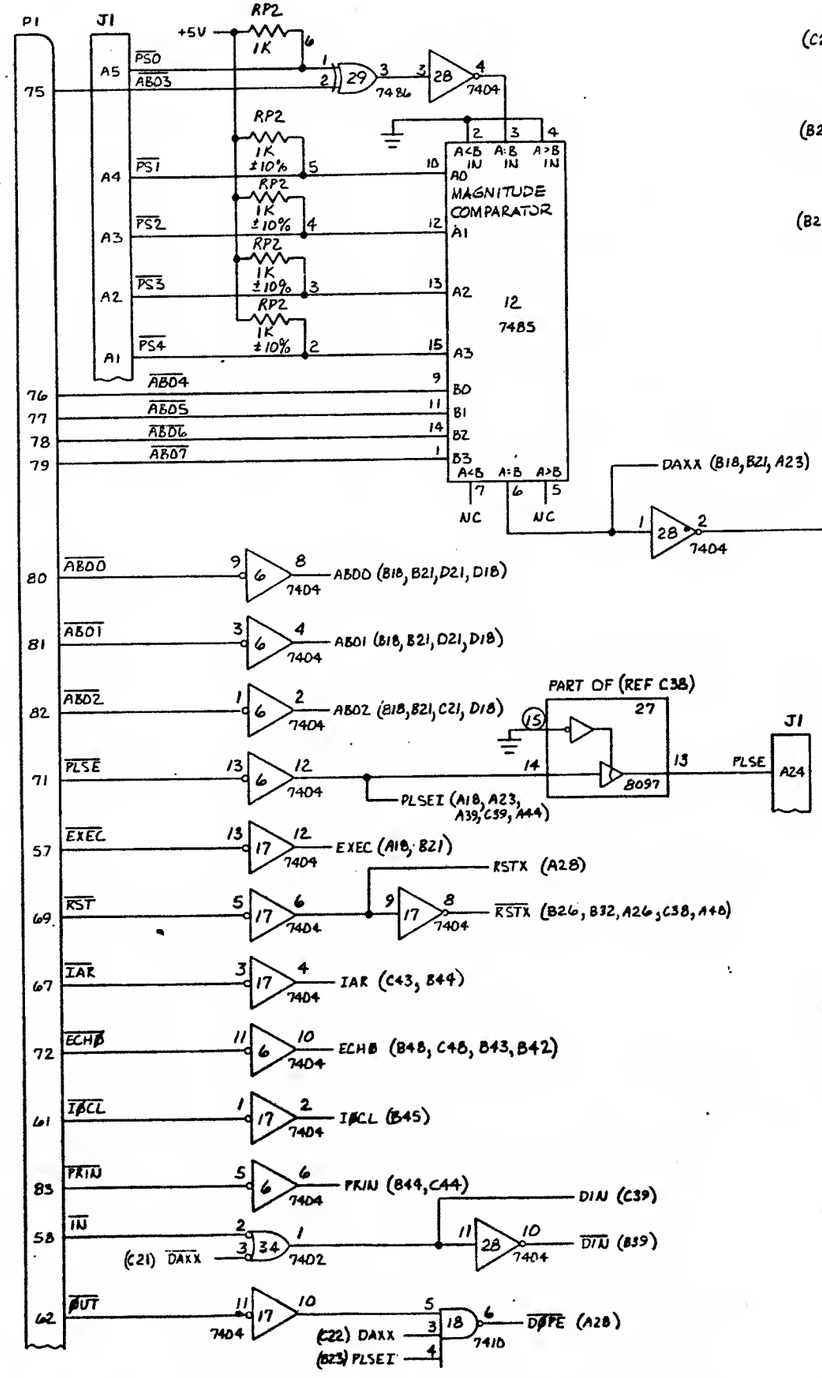
J1	SIGNAL	DIRECTION	LOCATION
B01	+5V	→	C7
02	+5V		B7
03	GND		B7
04			B7
05			B7
06			B7
07			B7
08			B7
09			B7
10			B7
11	GND	→	B7
12		N/U	
13		N/U	
14		N/U	
15		N/U	
16		N/U	
17		N/U	
18		N/U	
19	$\overline{IBF}$	←	B38
20	GND-C	→	B7
21	GND-C	→	B7
22	$\overline{STB}$	←	C25
23	AØ	←	C38
24	ØBS	←	A29
25	IBF	←	B38
26	ØBE	←	A29
27	ØBRS	→	A32
28	RNT2	→	C48
29	RNT1	→	C48
30	STB	←	C25
31	EST	→	C40
32	$\overline{ITRAN}$	→	D40
33	BC15	←	B27
34	BC14	←	B27
35	BC13	←	B27
36	BC12	←	B27
37	BC11	←	C27
38	BC10	←	C27
39	BC09	←	C27
40	BC08	←	C27
41	BC07	←	C27
42	BC06	←	C27
43	BC05	←	C27
44	BC04	←	D27
45	BC03	←	D27
46	BC02	←	D27
47	BC01	←	D27
48	BC00	←	D27
49	C3	→	C26
B50	C1	→	B26

J2	SIGNAL	DIRECTION	LOCATION
1	GND	→	B8
2			
3			
4			
5			
6			
7			
8	GND	→	B8
9	ØPØL	→	D30
10	RPØL	→	C48
11	SPØL	→	A32
12	$\overline{ITRAN}$	→	D40
13	IPØL	→	A37

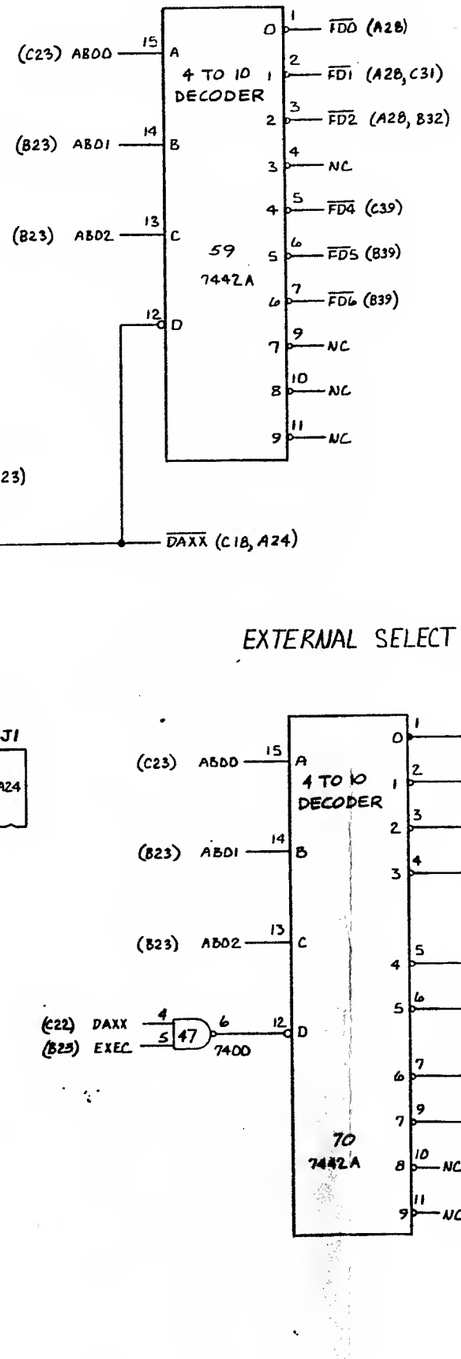
SIZE	DRAWING NO.	REV
D	00-52213-XX	G0
SCALE= —	SHT. 2 OF .	

REVISIONS					
REV	DESCRIPTION	DR	CH	DATE	APPROVAL

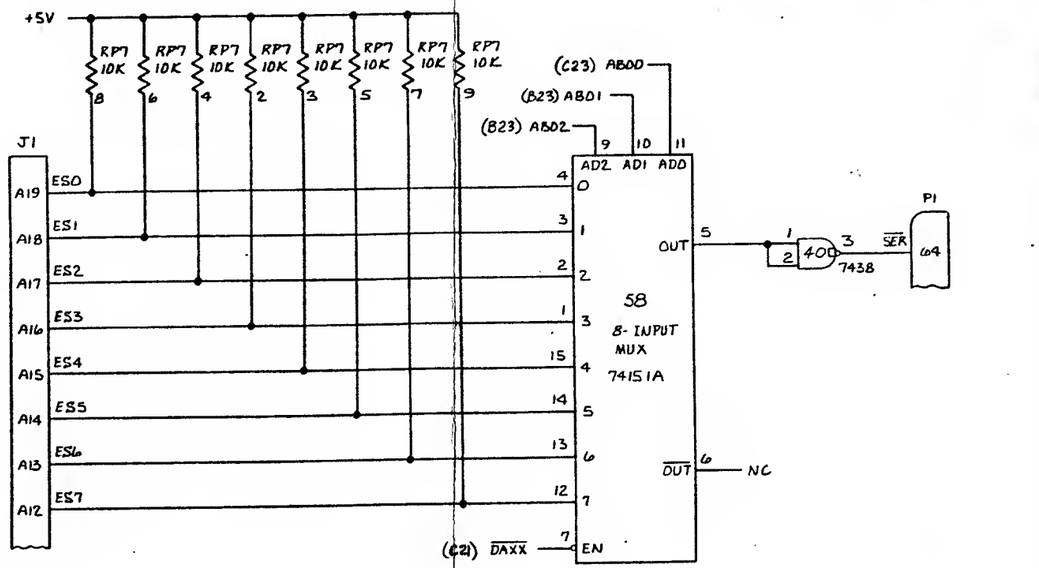
DEVICE ADDRESS DECODE



FUNCTION DECODE

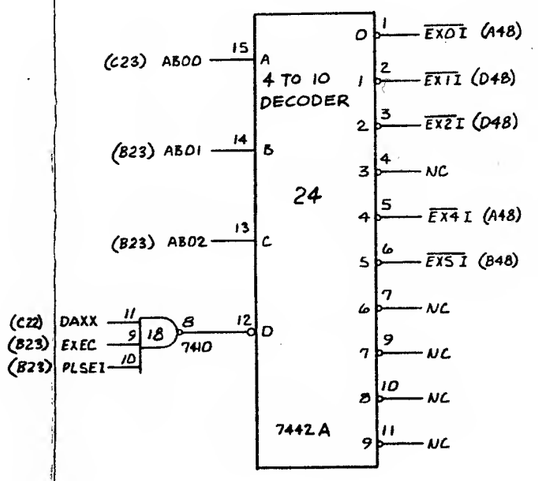


EXTERNAL SENSE



EXTERNAL SELECT COMMAND DECODE

INTERNAL SELECT COMMAND DECODE



SIZE		REV
D	00-52213-XX	60
SCALE: NONE	SHT. 3 OF	

REVISIONS

REV	DESCRIPTION	DR	CH	DATE	APPROVAL

OUTPUT DATA

OUTPUT STROBE

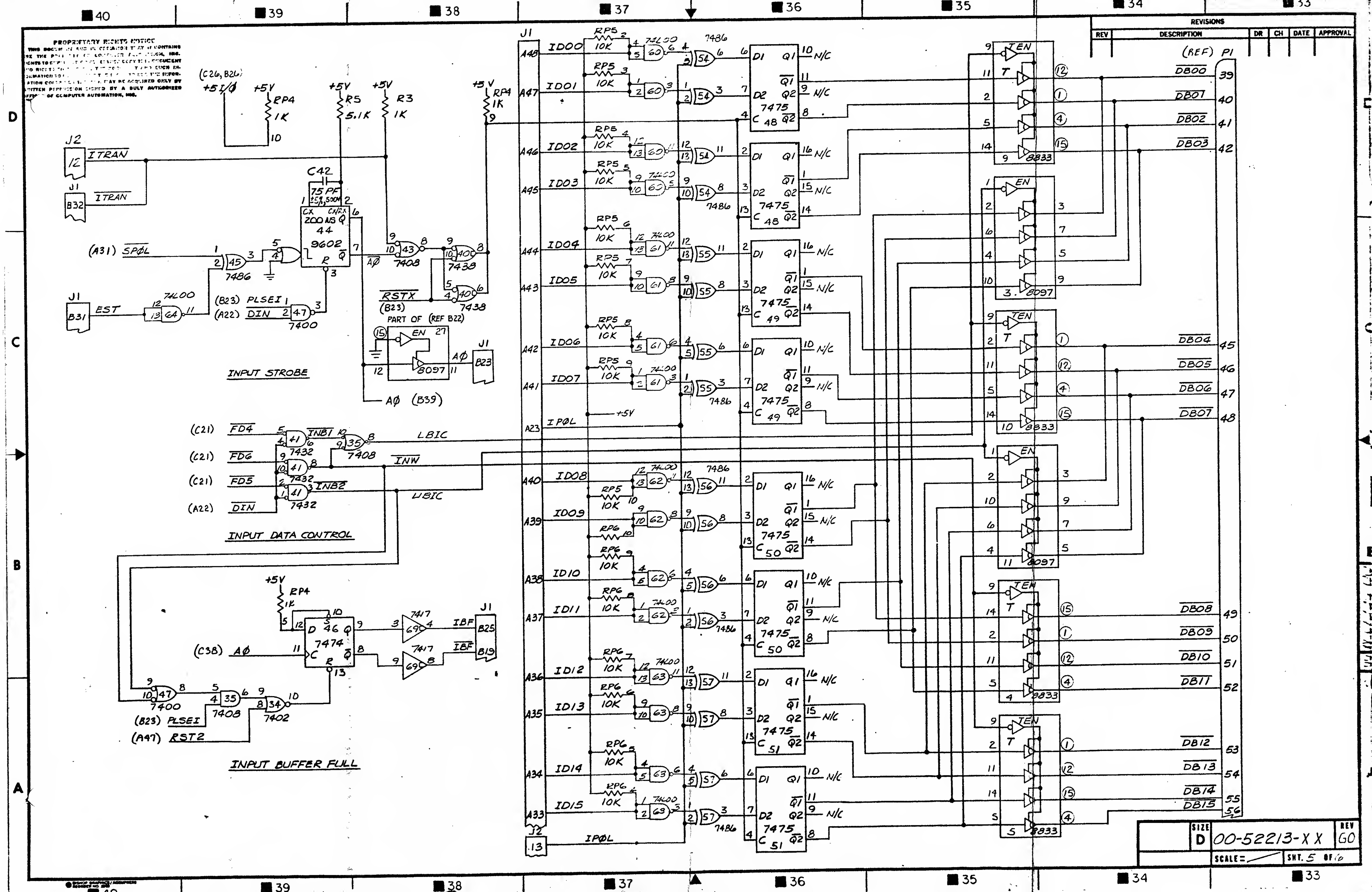
OUTPUT STROBE DELAY

OUTPUT BUFFER FULL

OUTPUT STEERING CONTROLS

SIZE	DRAWING NO.	REV
D	00-52213-XX	50

SCALE= SHT. 4 OF 5



REVISIONS
REV
DESCRIPTION
DR
CH
DATE
APPROVAL
INPUT STROBE
INPUT DATA CONTROL
INPUT BUFFER FULL
ITRAN
EST
(A31) SPØL
(B23) PLSEI
(A22) DIN
RSTX
(B23)
PART OF (REF B22)
AØ (B39)
(C21) FD4
(C21) FD6
(C21) FD5
(A22) DIN
INB1
INB2
LBIC
UBIC
INW
(C38) AØ
IBF
(B23) PLSEI
(A47) RST2
IPØL
(REF) P1
DB00
DB01
DB02
DB03
DB04
DB05
DB06
DB07
DB08
DB09
DB10
DB11
DB12
DB13
DB14
DB15
ID00
ID01
ID02
ID03
ID04
ID05
ID06
ID07
ID08
ID09
ID10
ID11
ID12
ID13
ID14
ID15
9602
7474
7475
7486
8833
8097
7400
7402
7408
7417
7432
7438
C42 75 PF
SIZE D
00-52213-XX
REV G0
SCALE
SHT. 5 OF 6

REVISIONS

REV	DESCRIPTION	DR	CH	DATE	APPROVAL

INTERRUPT 1 STIMULUS STORE F/F

INTERRUPT 1 WORDMASK F/F

INTERRUPT STIMULUS INPUTS

INTERRUPT 2 STIMULUS STORE F/F

INTERRUPT 2 WORD MASK F/F

INTERRUPT PRIORITY STORAGE REGISTER

INTERRUPT OFFSET

BLOCK INTERRUPT F/F'S.

INTERRUPT LOGIC

SIZE	DWG NO	REV
D	00-52213-XX	50

SCALE= SHT. OF

48 47 46 45 44 43 42 41

Computer Automation
FEB 04 1977

ENGINEERING NOTICE

SHEET 1 OF 2

MODEL	TITLE	EN NO.	REV.
LSI	ASSEMBLY-PWB 16 BIT I/O	4817	G1

CHARGE NO.	REA NO.	DFTSMN	CHK	DATE
7517	01783	EOC		5-27-76

TYPE OF CHANGE		OTHER EFFECTIVITY	DISPOSITION	U	R	S
DEVIATION			ACTIVITY			
RELEASE			ON ORDER			
DOC. CHANGE			IN STOCK		X	
FUNCTIONAL	X		IPP			
MANDATORY	X		IPT			
			FIN GOODS		X	
ABN ISSUED		PROGRAM AFFECTED / CAPABLE TEST	CUST RET			

COG ENG	CONT ENG	
TEST ENG	PROD MFG ENG	6/2/76
CAPABLE TEST	PURCH/MAT.CONT	6/2/76
TEST	FIELD SERV	6/9/76
Q.A.	CUST SERV	

DRAWINGS AFFECTED

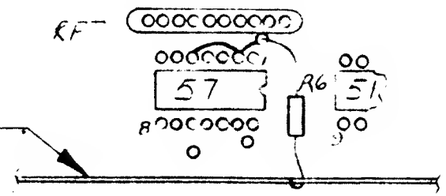
DWG NO.	DWG TITLE	INCORP DATE	REV IS	REV WAS
00-53213-XX	ASSY		G1	G0
00-53213-00,	B/M'S		G1	G0
-01				
00-52213-XX	LOGIC		G1	G0
00-51213-00	DETAIL (SHT 1)		G1	G0

REASON FOR CHANGE:

ADD A PULL UP ON IP/L

DESCRIPTION OF CHANGE | REWORK INSTRUCTIONS PER CAI WORKMANSHIP MANUAL 97002-00B

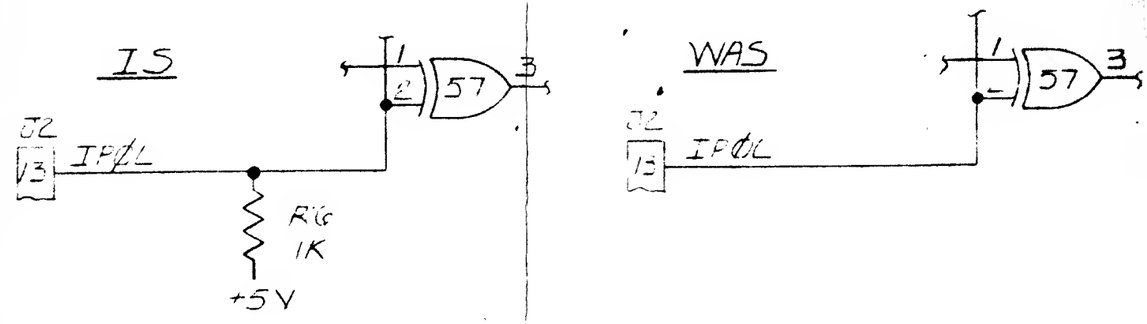
A. ADD COMPONENT
1. INSTALL RES. R6, P/I OF 43000-01 (RES, FILM/1 K, 1%, 5%, 1/4W), AS SHOWN:



B. 00-52213-XX LOGIC
1. SHT 1 OF 6; NOTE 2:
IS: LAST REF DESIGS USED: IC 70, R6, C 58, RP7.
WAS: " " : " , R5, " , " .

JAN 28 1977

B. 00-52213-XX LOGIC (CONT'D)
2. SHT. 5 OF 6, ZONE A3:



C. 00-53213-00, -01 B/M'S
1 ITEM 31 IS: P/N-08-43000-01, QTY-8, DESIG-RES,
F/F 1K 5% 1/4W, REF DESIG-R3, 10.
WAS: QTY-1, DESIG-RES, 1K, 5%, 1/4W, REF DESIG-R3.

D. 00-53213-XX ASSY
1 ADD RES. R6 TO F/D PER SEC. A1.

E. UPGRADE REVISION LEVEL ONLY
1. 00-51213-00 DETAIL (SHT 1)

EN 4817

COMPUTER AUTOMATION, INC.

SHEET 2 OF 2